



June 2010

H.264 DVR SoC IP Design Service

For 4 CH Video Surveillance SoCs

Vision Micro Technology, Inc. has been practicing its successful business showing excellence in product reliability, quality, service, technology and competitive prices since our 1st generation M-JPEG Codec solutions.

Now we'd like to introduce the innovative IP solution comes with most advanced H.264 video compression for video surveillance applications. Our new IP solution for 4 CH video security systems can make customer's video surveillance applications very affordable by accompanying built-in VGA controller with 3D Deinterlacer.

In addition, it supports up to the D1 level video quality along with superior OSD graphic display so there is no need for scarifying anything while being able to build up and appeal your cost-effective video surveillance products to your own customers. You can rely upon the richness in functionality while maintaining system reliability by adopting Our H.264 IP solution for your market competitive and unailing product line-up.

◆ Video Interface

- Time multiplexed ITU-R BT.656 inputs for Video Decoder (4 channels)
- ITU-R BT.656 Output for Video Encoder

◆ VGA Output Interface

- Support 3D Motion Adaptive Deinterlacer
- Resolution: 1280 x 1024

◆ Compression Engine : H.264

- Simultaneous multi-standard encoding (D1, CIF support)
- Unrestricted motion vector
- 1/2 and 1/4-pel accuracy full search motion estimation
- H.264 Intra-prediction (support 9 different mode)
- In-loop de-blocking filter for H.264
- Bit-rate control (CBR & VBR)
- State-of-art error concealment strategy
- De-blocking filter for post-processing

◆ Host Processor

- 32-bit RISC Processor, 162MHz,
- 8KB I- Cache/8KB D- Cache

◆ Infra-red Receiver Interface for Remote controller

◆ Multimedia Card and Secure Digital(MMC/SD) Host Controller

◆ Memory Interface

- SDRAM Controller
- Serial Flash Memory Controller (supports dual/quad I/O mode) for can direct booting from serial flash after power-on

◆ Graphic On Screen Display (GOSD)

- 8bpp, 16bpp Graphic On Screen Display, alpha blending

◆ ATA Host Controller

- Compatible ATA/ATAPI-5, Support PIO Transfer up to two devices

◆ USB On-The-Go (USBOTG) 2.0 Controller with PHY

- Support High Speed Connection (480Mbps)

◆ USB 1.1 interface for use of USB mouse or Keyboard

- Support up to two devices

◆ Universal Asynchronous Receiver/Transmitters (UARTx)

- Supports up to four

◆ Inter-IC (I2C) Master Interface

◆ Serial Audio Interface with Inter-IC Sound (I2S)

◆ SPI Host Controller

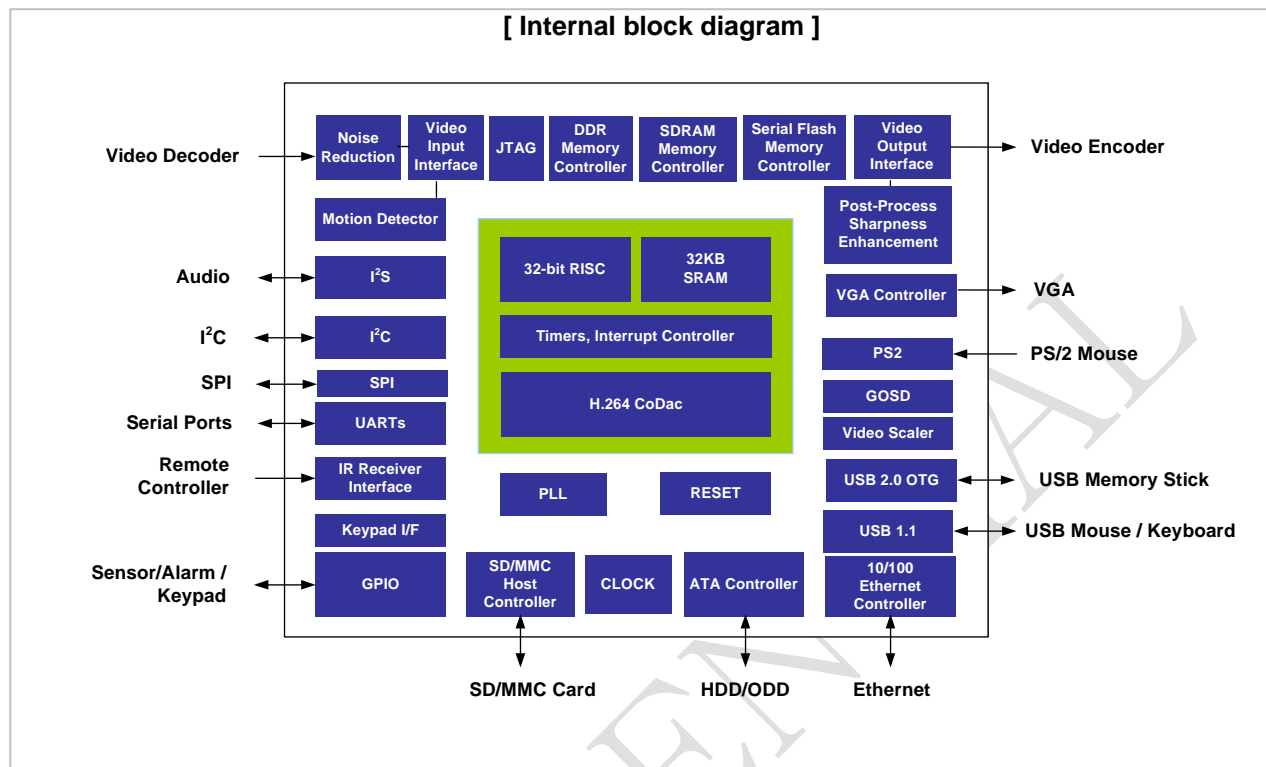
◆ Keypad Interface

◆ 10/100 Ethernet Controller

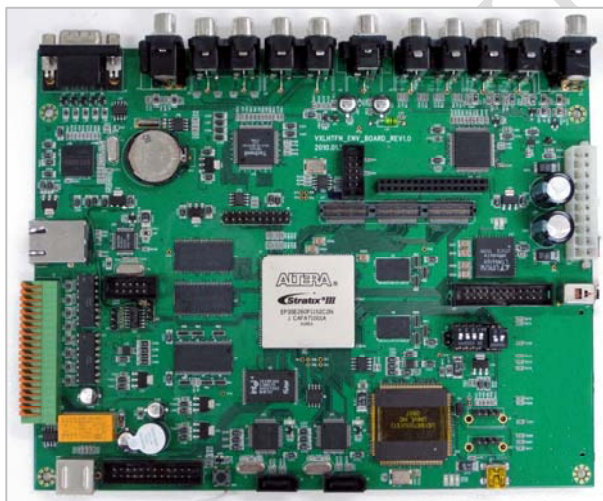
◆ General-Purpose I/O (GPIO) Ports

◆ PS/2 Host Controller

[Internal block diagram]



[FPGA Board]



Our IP solution is fully tested and technically proven on FPGA level.

Customers can verify all functionalities & overall operations of our IP solution in order to check its reliability using our FPGA test board.

Contact Us

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